Agenda of the HiPEAC Workshop at February 19, 2019.

HiPEAC - The European Network of Excellence on High Performance and Embedded Architecture and Compilation coordinates research, facilitates collaboration and networking, and stimulates commercialization in the areas of computer hardware and software research

Location: EDI (Institute of Electronics and Computer Science), 14 Dzerbenes st, Riga, Latvia, LV-1006 www.edi.lv

9:00 – 9:30	**Registration**
9:30 – 9:45	**Opening session**
	Welcome, Dr. Modris Greitans, Scientific Director, EDI
	Introduction of attendees

9:45 – 10:30　　HiPEAC Network Overview and Research at University of Ghent, *Prof. Koen De Bosschere*

10:30 – 11:00　　**Coffee Break/Networking**

11:00 – 11:40　　TETRAMAX Overview and Research at RWTH Aachen University, *Prof. Rainer Leupers*

11:40 – 12:00　　Presentation on research at EDI, *Dr. Kaspars Ozols*

12:00 – 13:00　　**Lunch / Networking**

13:00 – 14:00　　**ICT Research and Innovation in Latvian Universities and Institutes**
- RTU ETF (Faculty of electronics and telecommunications), Jurģis Poriņš, www.rtu.lv
- RTU MISIK (Department of artificial intelligence and systems engineering), Egons Lanvendelis, www.misik.rtu.lv
- UL IMCS (The Institute of Mathematics and Computer Science), Māris Alberts, www.lu.lv
- TTI (Transport and telecommunication institute), Irina Jackiva, www.tsi.lv

14:00 – 15:00　　**ICT Research and Innovation in Latvian Companies (1)**
- Hansamatrix group, Dr. Krišs Osmanis, www.hansamatrix.com
- LASMA, Lauris Bērziņš , www.lasma.lv
- LATTELECOM, Edmunds Garancs, www.lattelecom.lv
- LMT, Ingmārs Pūķis, www.lmt.lv

15:00 – 15:30　　**Coffee Break / Networking**

15:30 – 16:45　　**ICT Research and Innovation in Latvian Companies (2)**
- APPLY, Vismands Menjoks, www.applyit.lv
- BIOSAN, Vasīlijs Bankovskis, www.biosan.lv
- CITINTELLY, Aivars Ševerdaks, www.citintelly.com
- DataGroup, Jānis Irbe, www.kc.lv
- MAKSIKOMS, Eižens Putniņš, www.maksikoms.lv

16:45 – 17:00　　**Closing Remarks**

17:00 – 17:30　　**Networking**